EXHIBIT B19

☐ VirgilYP / peng Public

<> Code ⊙ Issues \$\frac{1}{2}\$ Pull requests ⊙ Actions ⊞ Projects ⊕ Security ⊬ Insights

^ያ 94451b22fc → ···

peng / ext / hal / nxp / imx / drivers / ccm_imx7d.c

diegosueiro ext/hal/nxp/imx: Import the nxp imx7 freertos bsp ...

At 1 contributor

85 lines (76 sloc) | 3.76 KB /* 1 2 * Copyright (c) 2015, Freescale Semiconductor, Inc. * All rights reserved. 3 4 * Redistribution and use in source and binary forms, with or without modification, 5 * are permitted provided that the following conditions are met: 6 7 * o Redistributions of source code must retain the above copyright notice, this list 8 9 of conditions and the following disclaimer. 10 * o Redistributions in binary form must reproduce the above copyright notice, this 11 list of conditions and the following disclaimer in the documentation and/or 12 other materials provided with the distribution. 13 14 * o Neither the name of Freescale Semiconductor, Inc. nor the names of its 15 contributors may be used to endorse or promote products derived from this 16 software without specific prior written permission. 17 18 * THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS "AS IS" AND * ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED 20 * WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE ARE 21 22 * DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT HOLDER OR CONTRIBUTORS BE LIABLE FOR * ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES 23 * (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; 24 * LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON 25 * ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT 26 * (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS 27 * SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE. 28 29 */


```
30
31
    #include "ccm_imx7d.h"
32
    /*****************************
33
34
    * Code
    **************************************
35
36
    37
38
39
    * Function Name : CCM SetDivider
40
    * Description : Set root clock divider
41
    42
43
    void CCM_SetRootDivider(CCM_Type * base, uint32_t ccmRoot, uint32_t pre, uint32_t post)
44
45
      assert (pre < 8);</pre>
46
      assert (post < 64);
47
48
      CCM_REG(ccmRoot) = (CCM_REG(ccmRoot) &
49
                     (~(CCM TARGET ROOT PRE PODF MASK | CCM TARGET ROOT POST PODF MASK))) |
                    CCM_TARGET_ROOT_PRE_PODF(pre) | CCM_TARGET_ROOT_POST_PODF(post);
50
51
    }
52
    53
54
55
    * Function Name : CCM GetDivider
56
    * Description : Get root clock divider
57
    58
59
    void CCM_GetRootDivider(CCM_Type * base, uint32_t ccmRoot, uint32_t *pre, uint32_t *post)
60
61
      assert (pre && post);
62
63
      *pre = (CCM_REG(ccmRoot) & CCM_TARGET_ROOT_PRE_PODF_MASK) >> CCM_TARGET_ROOT_PRE_PODF_SHIFT;
64
      *post = (CCM_REG(ccmRoot) & CCM_TARGET_ROOT_POST_PODF_MASK) >> CCM_TARGET_ROOT_POST_PODF_SHIFT
65
66
67
    68
69
    * Function Name : CCM_UpdateRoot
70
    * Description : Update clock root in one step, for dynamical clock switching
71
    72
73
    void CCM_UpdateRoot(CCM_Type * base, uint32_t ccmRoot, uint32_t mux, uint32_t pre, uint32_t post)
74
   {
75
      assert (pre < 8);</pre>
76
      assert (post < 64);</pre>
77
78
      CCM_REG(ccmRoot) = (CCM_REG(ccmRoot) &
```

##